



Docket No. SON-2810

Application No. Filing Date Examiner **Art Unit** 10/647,217-Conf. #1901 August 26, 2003 D. B. Gandhi 2138

Applicant(s): Yoshitaka Kayukawa et al.

Invention: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD FOR TESTING SAME

TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an amendment in the above-identified application.

		CLAIM	S AS AMENI	DED	
	Ciaims Remaining After Amendment	Highest Number Previously Paid	Number Extra Ciaims Present	Rate	
Total Claims	20	- 23 =		х	
Independent Claims	5	- 7 =		x	
Multiple Depend	lent Claims (che	eck if applicabl	e)		
Other fee (pleas	e specify): E	xtension for res	ponse within s	econd month	
TOTAL ADDIT	IONAL FEE FO	R THIS AME	NDMENT:		
x Large Entity				Small Entit	.y
	al fee is require	d for this amer	ndment	<u></u>	•
A duplicate	ge Deposit Acc			the amount of \$	·
A check in th	ne amount of \$		to cover	the filing fee is en	closed.
Payment by	credit card. Fo	orm PTO-2038	is attached.		
	ris hereby auth below. A dup	•	•	Deposit Account enclosed.	No. <u>18-0013</u>
x Credit a	ny overpaymen	it. /			·
	1		n processina f	iees required under	r 37 CFR 1.16 and 1.17.
	11 ×	4	4 20 n	ooo roqan oa arrao.	
			727	Dated:	April 30, 2007
Ronald P. Kana Attorney/Agent	· /		~		
RADER, FISHN 1233 20th Stree Suite 501 Washington, D0	et, N.W.	R PLLC			